

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L28	0	(clock distribution circuit AND first clock circuit AND first clock signal AND second clock circuit AND second clock signal AND error signal AND input AND clock input). clm.	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/03/28 16:08

3/ 28/ 09 4:08:54 PM
**C:\ Documents and Settings\ pchung\ My Documents\ EAST\ Workspaces
 \ 09919372.wsp**